L Number	Hits	Search Text	DB	Time stamp
1	231	(memory adj device) same (internal with memory with bus) same (external with memory) same control\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/30 13:32
2	110	365/\$.ccls. and (memory adj device) same (internal with memory with bus) same (external with memory) same control\$4	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/30 13:33
3	48	365/230.03,238.5,63,51,201,226-229.ccls. and (memory adj device) same (internal with memory with bus) same (external with memory) same control\$4	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/30 13:35
4	40	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with memory with bus) same (external with memory) same control\$4	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/30 13:44
5	1	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with memory with bus) same (external with memory) same control\$4 same (page with mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/30 13:46
7	38	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with memory with bus) same (external with memory with bus) same control\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/30 13:50
9	1	"5912849".PN.	USPAT	2004/09/30 13:49
12	1	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with bus) same (external with bus) same control\$4 same (page with mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/30 13:52
13	13	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with bus) same (external with bus) same control\$4 same mode	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/30 13:53
14	1	"5912849".PN.	USPAT	2004/09/30 13:53
15	5	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with bus) same (external with bus) same \$5control\$4 same test\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/30
16	5	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with control\$4) same (external with control\$4) same bus same test\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/30 13:56
.17	47	365/230.03,238.5,63,51,201,145.ccls. and (memory adj device) same (internal with control\$4) same bus	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/30 14:01
18	2	6,629,185.pn.	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/30 14:01
-	9	asynchronous same synchronous same (page adj mode) same (burst adj mode)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/27 13:09

-	56	asynchronous and synchronous same (page adj mode) and (burst adj mode)	USPAT; US-PGPUB; EPO; JPO;	2004/09/27 13:09
			DERWENT; IBM TDB	
-	31	365/\$.ccls. and asynchronous and synchronous same (page adj mode) and (burst adj mode)	USPĀT; US-PGPUB; EPO; JPO;	2004/09/27 13:36
_	42	365/\$.ccls. and asynchronous same	DERWENT; IBM_TDB USPAT;	2004/09/27
		synchronous same page same burst	US-PGPUB; EPO; JPO; DERWENT;	13:50
_	1	365/\$.ccls. and asynchronous same synchronous same page same burst same (memory adj bus) same (system adj bus) same (memory adj controller)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/27 13:52
_		asynchronous same synchronous same page same burst same (memory adj bus) same (system adj bus) same (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/27 13:55
-	9	365/\$.ccls. and asynchronous and synchronous and page and burst and (memory adj bus) and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/28 09:37
-	11	("4850891" "5721860" "5790447" "5982655" "6067593" "6078515" "6111757" "6173382" "6285558" "6298426" "6353539").PN.	USPAT	2004/09/27 14:14
-	8	("5379304" "5390308" "5412788" "5450422" "5745914" "5765188" "5860134" "5897663").PN.	USPAT	2004/09/27 14:18
-	34	365/\$.ccls. and page and burst and (memory adj bus) and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/27 14:31
-	1	365/\$.ccls. and page and burst and FeRAM and (memory adj bus) and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/27 14:31
_	4	365/\$.ccls. and page and burst and FeRAM	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/27 14:34
~	310	365/\$.ccls. and FeRAM	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/27 14:34
_	1	365/\$.ccls. and (memory adj bus) and FeRAM and (system adj bus) and (memory adj controller)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/27 14:35
_	1	(memory adj bus) and FeRAM and (system adj bus) and (memory adj controller)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/27 14:35
-	4645	(memory adj bus) and (system adj bus) and (memory adj controller)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/27 14:36

-	6	365/\$.ccls. and SRAM and SDRAM and (memory adj bus) and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO;	2004/09/27 14:37
			DERWENT; IBM_TDB	
-	1	((asynchronous and (synchronous with page) and (synchronous with burst)) with (memory adj controller)) and (memory adj bus) and (system adj bus) and (memory adj	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/28 09:42
-	1	controller) ((asynchronous and (synchronous with	IBM_TDB USPAT;	2004/09/28
		<pre>page) and (synchronous with burst)) with (memory adj controller)) and (memory adj bus) and (system adj bus)</pre>	US-PGPUB; EPO; JPO; DERWENT; IBM TDB	09:43
-	1	<pre>((asynchronous and (synchronous with page) and (synchronous with burst)) with (memory adj controller))</pre>	USPĀT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/28 09:42
-	3	((asynchronous and page and burst) with (memory adj controller)) and (memory adj bus) and (system adj bus)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/28 09:51
-	6	((SRAM same SDRAM same DRAM) with (memory adj controller\$1)) and (memory adj bus) and (system adj bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/28 09:56
-	94	((SRAM same SDRAM same DRAM) with (memory adj controller\$1))	USPĀT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/28 09:56
-	8	365/\$.ccls. and ((SRAM same SDRAM same DRAM) with (memory adj controller\$1))	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/28 10:07
-	3	6,215692.pn.	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/28 10:07
-	96	asynchronous and synchronous and page and burst and (memory adj bus) and (system adj bus) and (memory adj controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/28 13:29
-	11	("4850891" "5721860" "5790447" "5982655" "6067593" "6078515" "6111757" "6173382" "6285558"	USPAT	2004/09/28 11:56
-	3	"6298426" "6353539").PN. ("5604883" "5649161" "5664153").PN.	USPAT	2004/09/28 12:13
-	427	(memory adj bus) and synchronous adj3 (FeRAM or memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/28
_	89	((memory adj bus) and synchronous adj3 (FeRAM or memory)) and (page adj mode)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/28 13:31
_	82	(((memory adj bus) and synchronous adj3 (FeRAM or memory)) and (page adj mode)) and (controller or (control adj unit) or (control adj circuit))	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/28 13:32
			IBM_TDB	

-	72	((((memory adj bus) and synchronous adj3	USPAT;	2004/09/28
		(FeRAM or memory)) and (page adj mode))	US-PGPUB;	13:32
	1	and (controller or (control adj unit) or	EPO; JPO;	
	1	(control adj circuit))) and external	DERWENT;	
			IBM TDB	
-	17	((((memory adj bus) and synchronous adj3	USPĀT;	2004/09/28
		(FeRAM or memory)) and (page adj mode))	US-PGPUB;	13:36
		and (controller or (control adj unit) or	EPO; JPO;	
1		(control adj circuit))) and (external	DERWENT;	
		adj2 bus)	IBM TDB	
1 _	7	(((((memory adj bus) and synchronous adj3	USPAT;	2004/09/28
	·	(FeRAM or memory)) and (page adj mode))	US-PGPUB;	13:38
		and (controller or (control adj unit) or	EPO; JPO;	13.30
		(control adj circuit))) and (external	DERWENT;	
		adj2 bus)) and (burst adj mode)		1
_	362		IBM_TDB	2004/00/28
	302		USPAT;	2004/09/28
		(FeRAM or memory)) and controller	US-PGPUB;	13:39
			EPO; JPO;	
			DERWENT;	1
			IBM_TDB	1
_	8927	memory and (external adj2 bus)	USPAT;	2004/09/28
1			US-PGPUB;	13:39
1			EPO; JPO;]
1			DERWENT;	
			IBM TDB	
_	85	(((memory adj bus) and synchronous adj3	USPAT;	2004/09/28
		(FeRAM or memory)) and controller) and	US-PGPUB;	13:40
		(memory and (external adj2 bus))	EPO; JPO;	
		(DERWENT;	
	66		IBM_TDB	2004/00/00
_	00	[USPAT;	2004/09/28
		(FeRAM or memory)) and controller) and	US-PGPUB;	13:41
		(memory and (external adj2 bus))) and	EPO; JPO;	
		(transfer adj2 data)	DERWENT;	
			IBM_TDB	
-	27		USPAT;	2004/09/28
		(FeRAM or memory)) and controller) and	US-PGPUB;	13:42
		(memory and (external adj2 bus))) and	EPO; JPO;	
		(transfer adj2 data) and (external adj2	DERWENT;	
		control\$4)	IBM TDB	
_	2		USPĀT;	2004/09/30
		6,526451.pn.	US-PGPUB;	07:35
		-	EPO; JPO;	1
			DERWENT;	
			IBM TDB	ļ
_	2	6,125,061.pn.	USPĀT;	2004/09/30
:			US-PGPUB;	13:20
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
 _	2	6,266,285.pn.	USPAT;	2004/09/30
1	-	0,200,200.pii.		1
			US-PGPUB;	13:21
1			EPO; JPO;	
			DERWENT;	
	_	6 406 016	IBM_TDB	
-	1	6,426,916.pn.	USPAT;	2004/09/30
			US-PGPUB;	13:21
			EPO; JPO;	
1			DERWENT;	
			IBM_TDB	
-	1	6,415,339.pn.	USPĀT;	2004/09/30
]			US-PGPUB;	13:21
			EPO; JPO;]
1			DERWENT;	
			IBM TDB	1
1 -	2	6,314,251.pn.	USPAT;	2004/09/30
	-	-,,	US-PGPUB;	13:22
			EPO; JPO;	
			DERWENT;	
[
_	,	"6024901" DN	IBM_TDB	3004/00/30
-	1	"6034891".PN.	USPAT	2004/09/30
				13:22